

REMARKS

Claims 1-17 are pending in the application. Claims 1-16 are rejected. Claim 17 has been cancelled.

Claims 1-6, 9, and 11-12 are rejected under 35 USC 103(a) as being unpatentable over Fischer and Kim. Claims 1 and 11 include ... outputting a reset signal from the tracking circuit after the predetermined tracking time, thereby disabling the internal memory clock signal The references of record do not show, teach, or suggest the above limitations of claims 1 and 11. The Fischer reference does not disclose disabling the internal memory clock signal. Claims 2-10 depend from claim 1. Claims 12-16 depend from claim 11. Therefore, claims 1-16 are believed to be allowable over the references of record.

It is believed that the above remarks are fully responsive to the Official Action. Reconsideration and allowance are therefore respectfully requested.

Respectfully submitted,

/Alan K. Stewart/
Alan K. Stewart
Registration No. 35,373

Texas Instruments, Incorporated
P. O. Box 655474 - M/S 3999
Patent Department
Dallas, Texas 75265